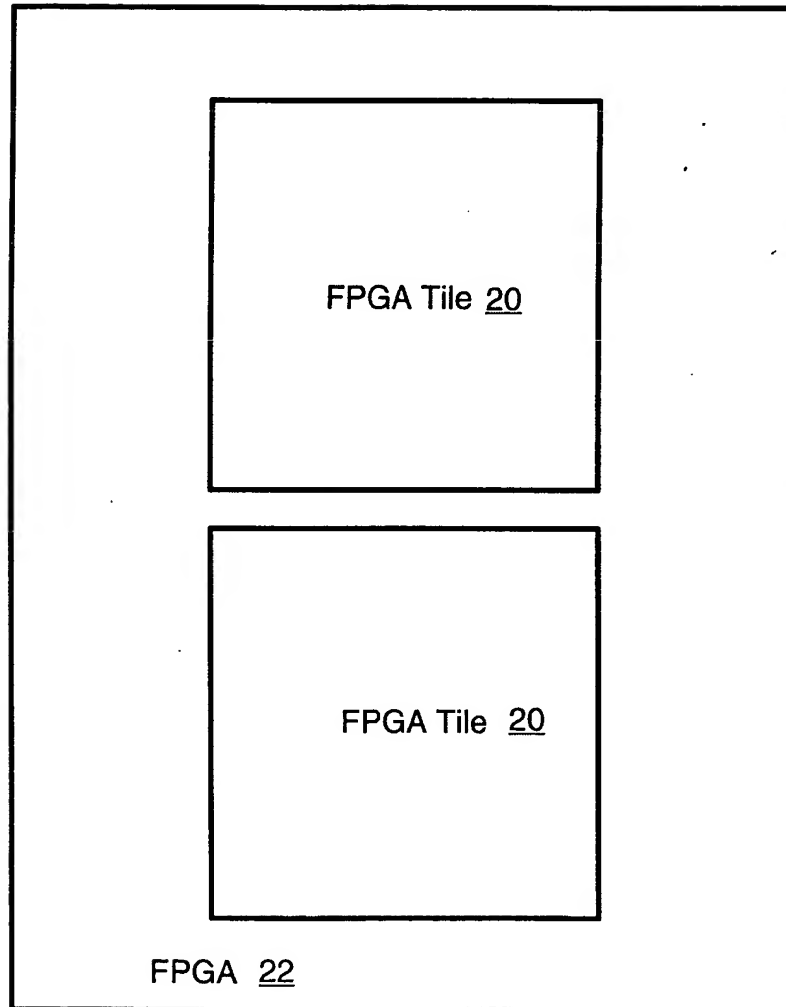


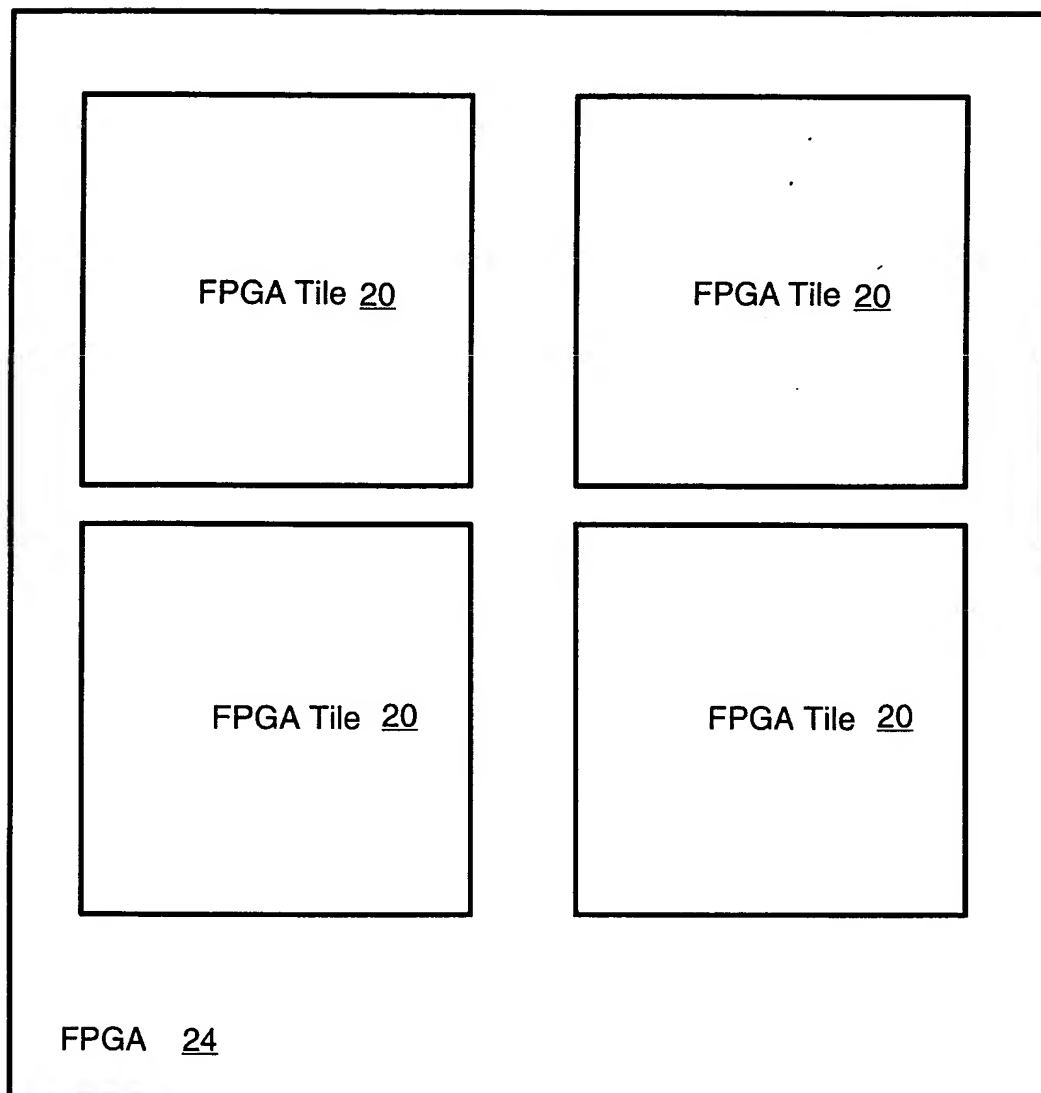
**FIG. 1**

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**FIG. 2**

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**FIG. 3A**

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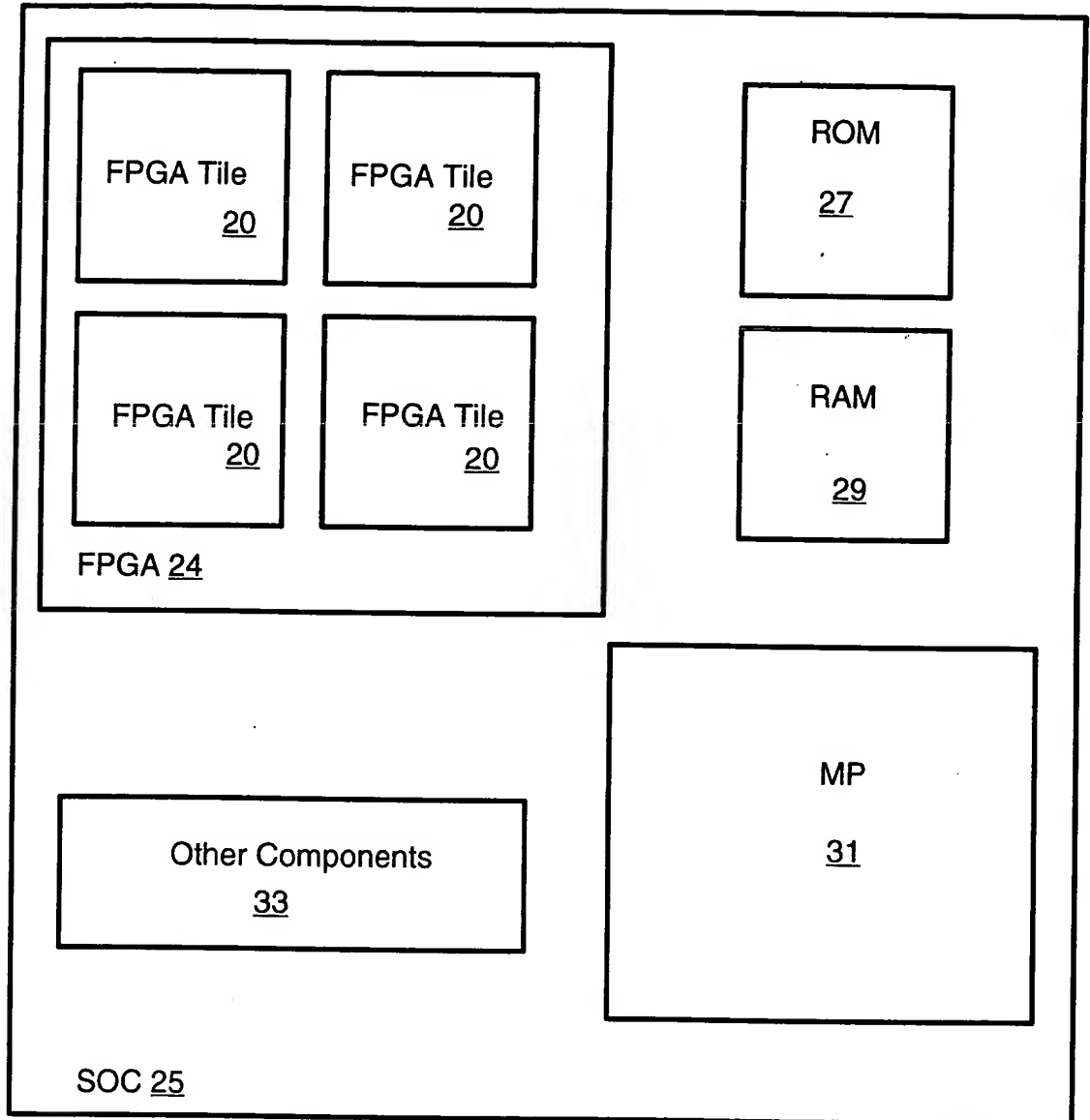
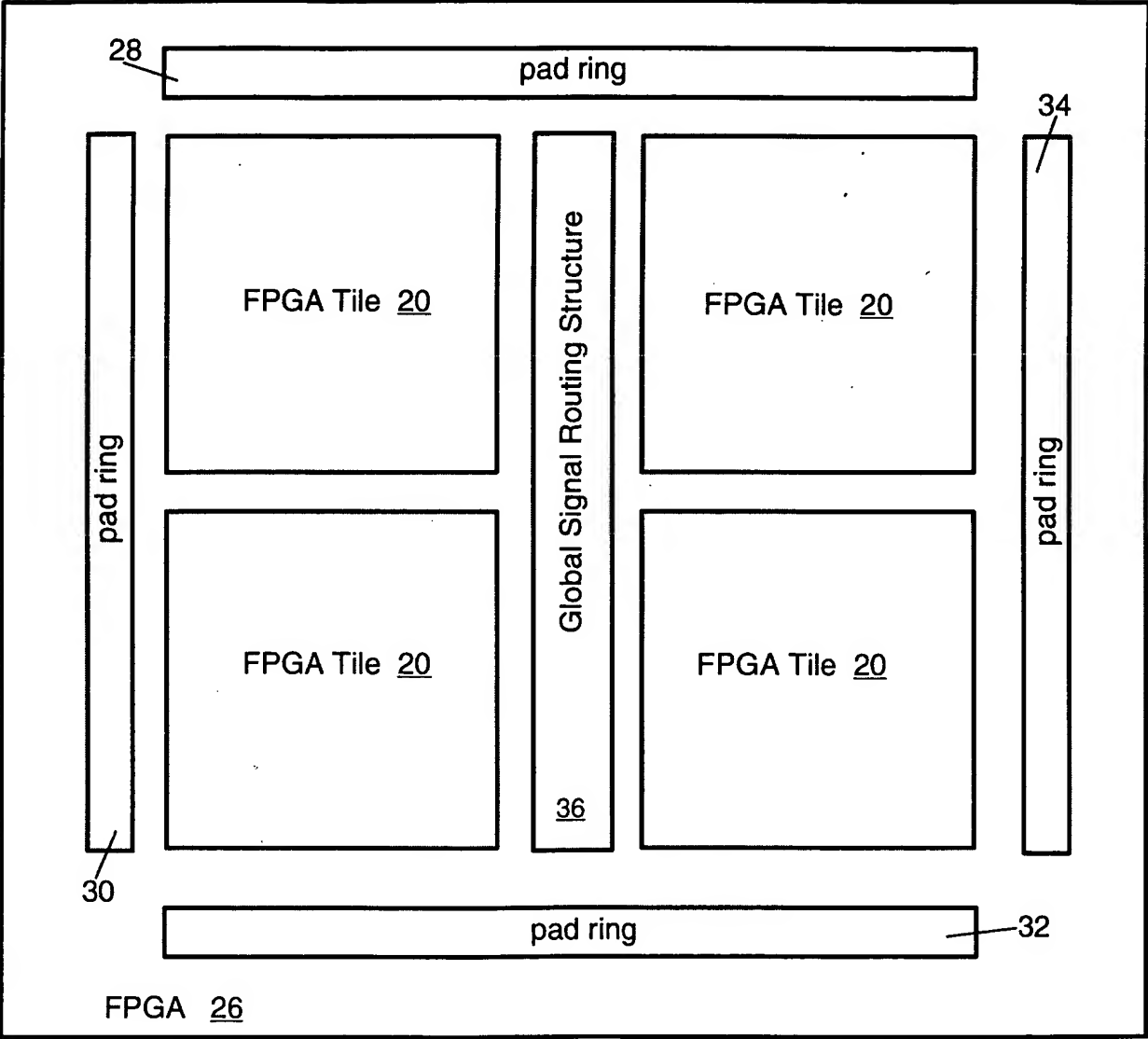


FIG. 3B



**FIG. 4**

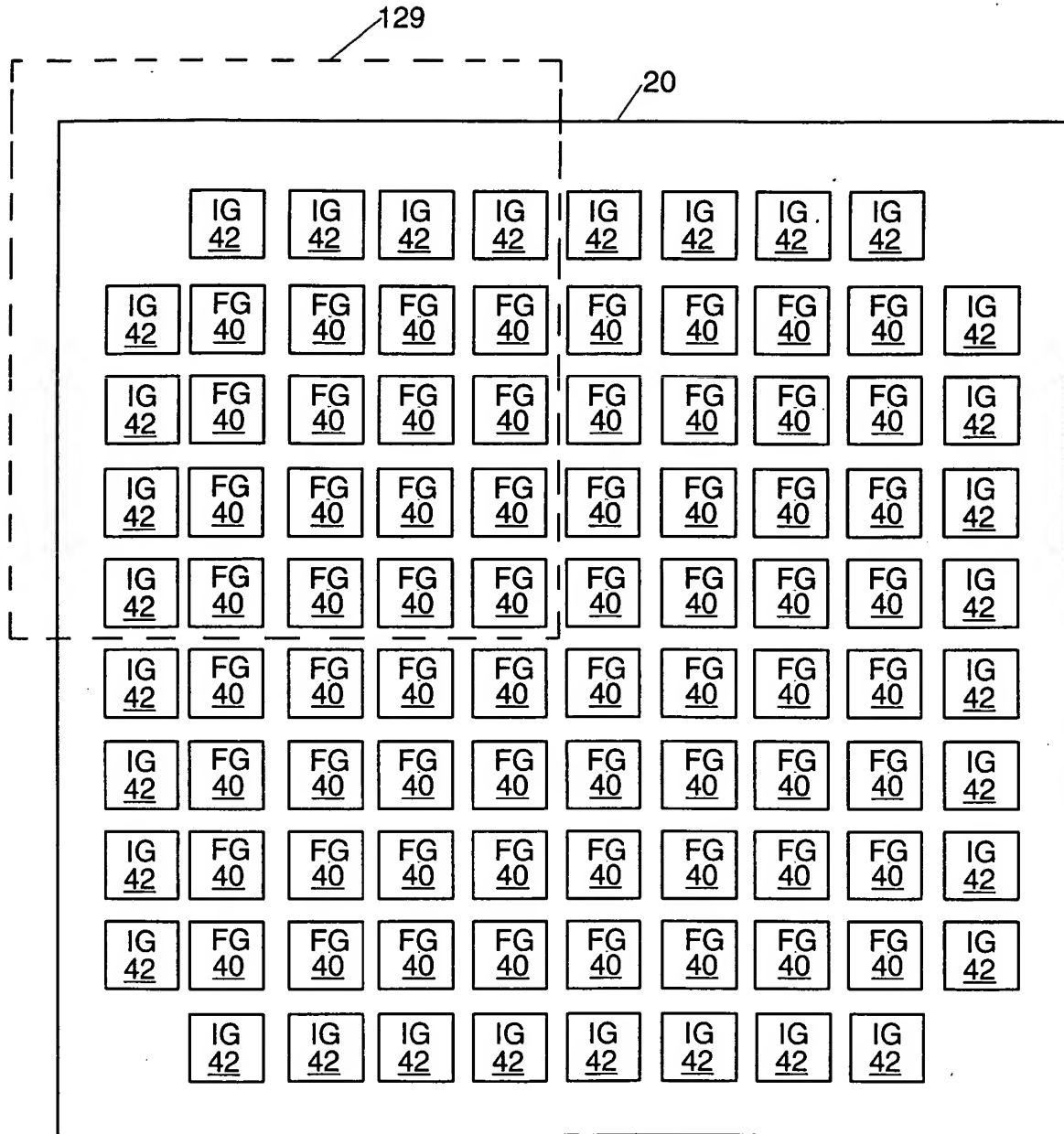


FIG. 5

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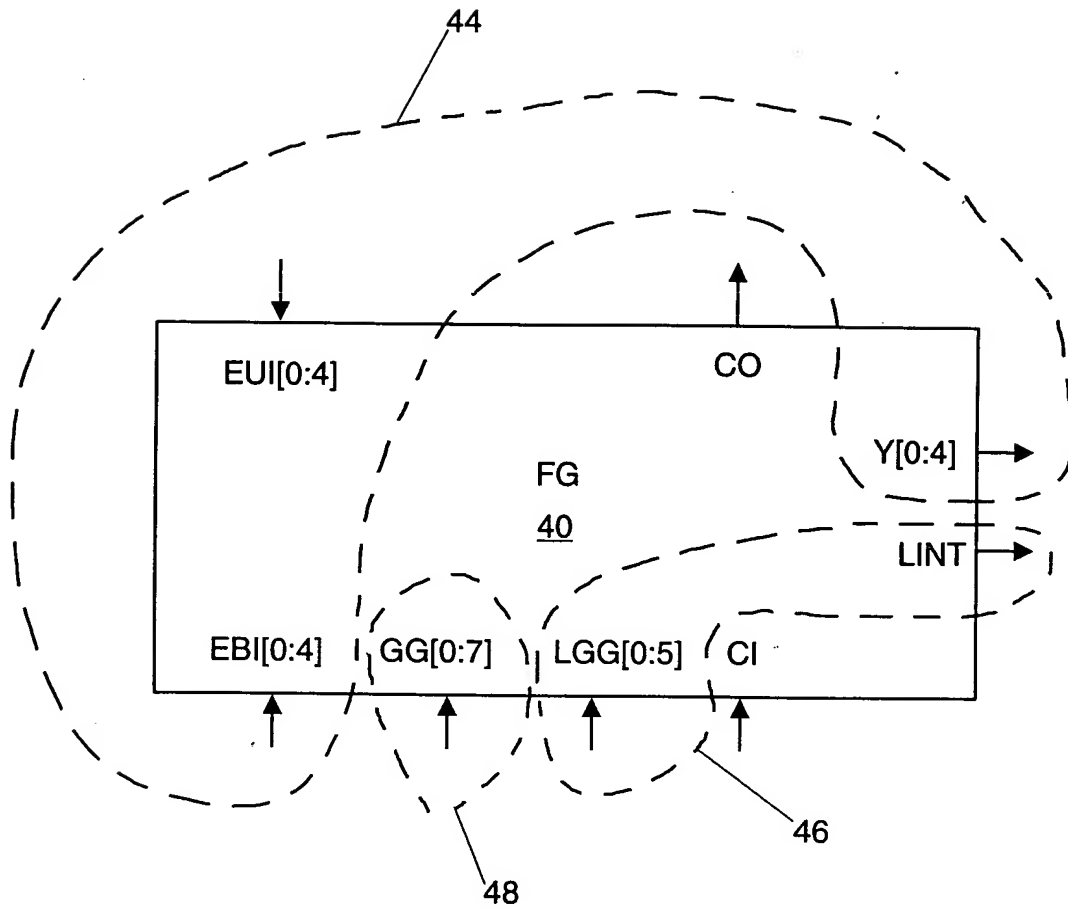


FIG. 6

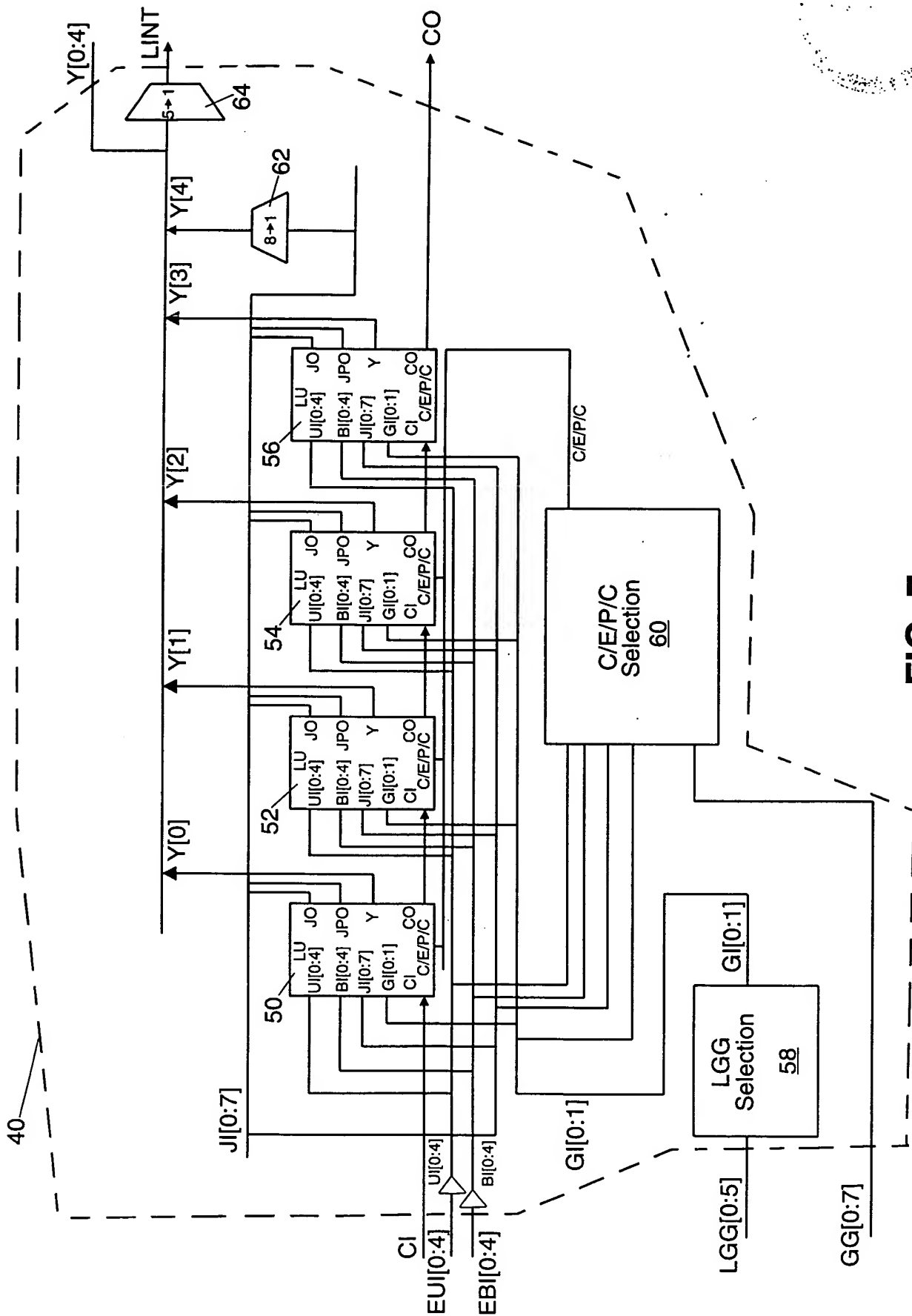
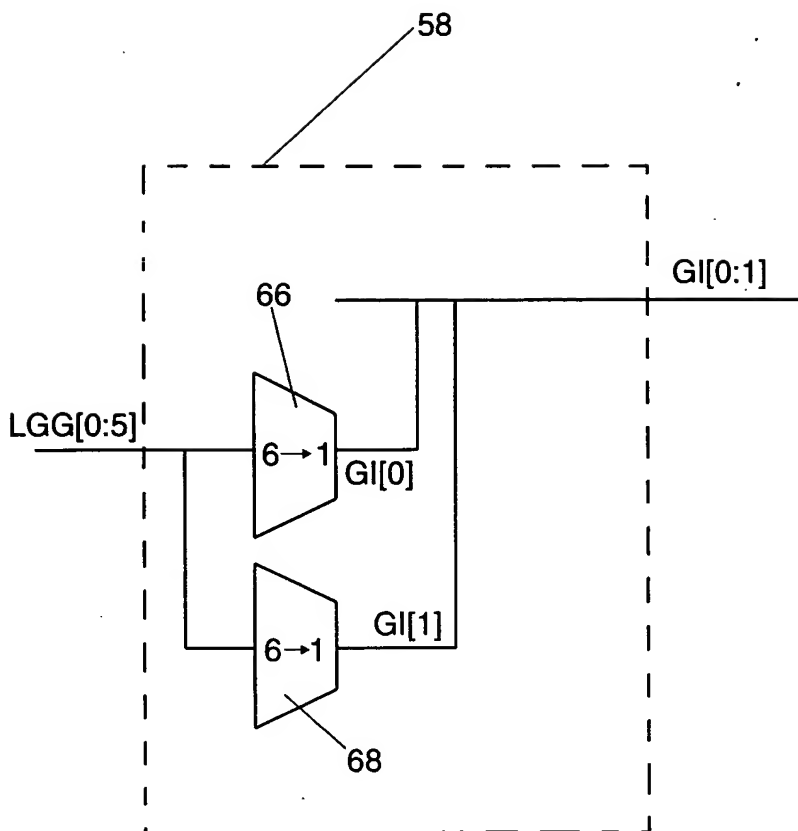


FIG. 7





**FIG. 8**

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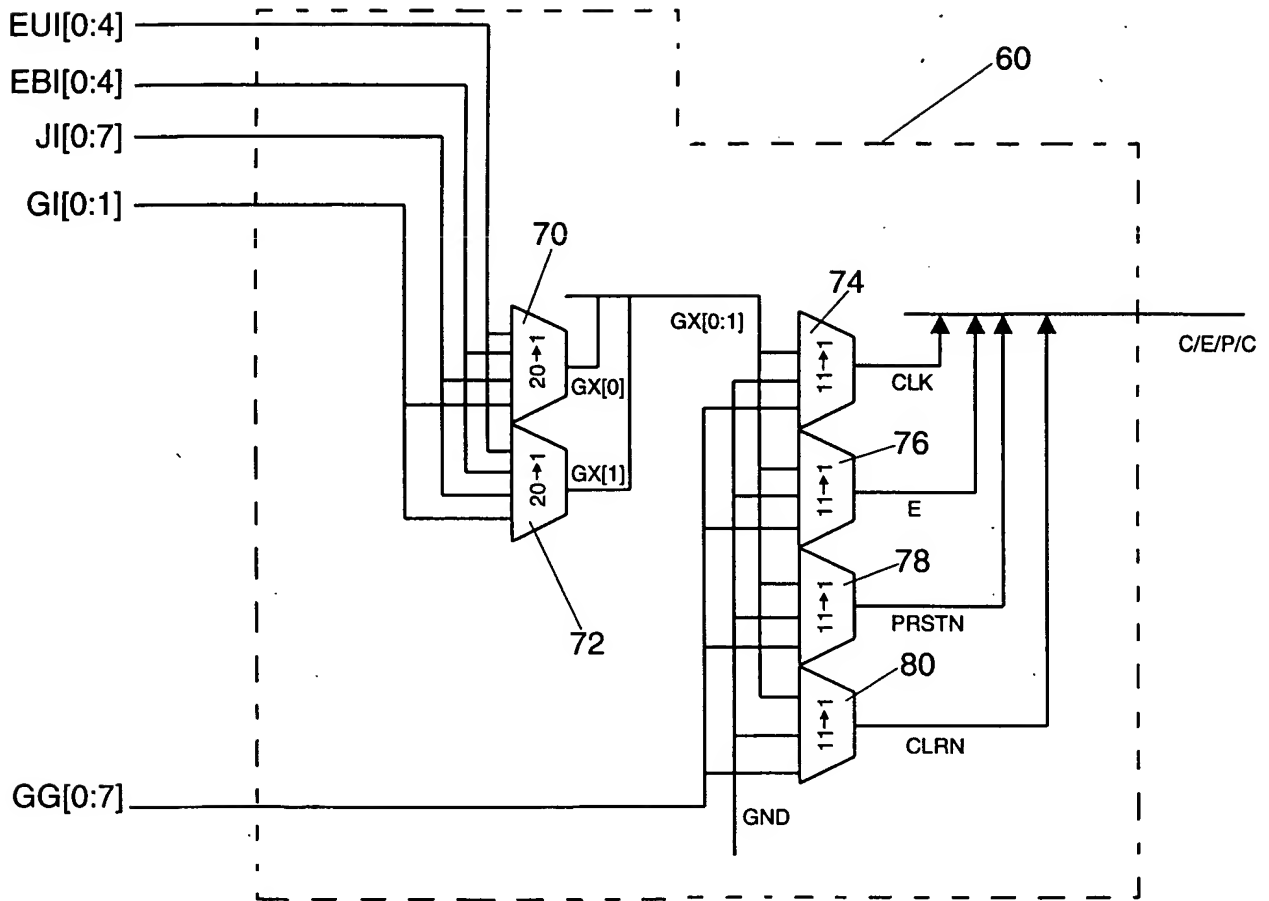
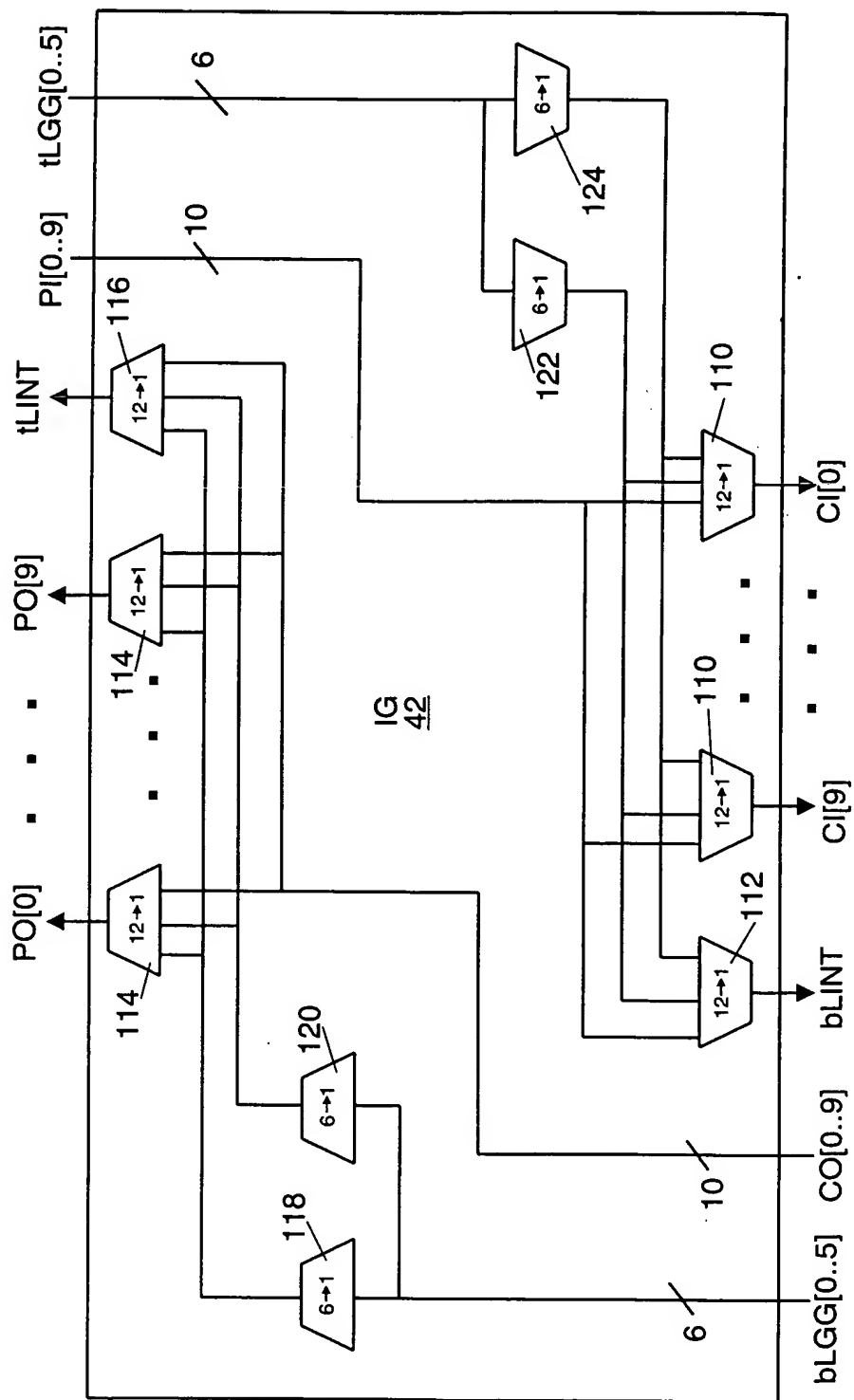


FIG. 9

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**FIG. 11**

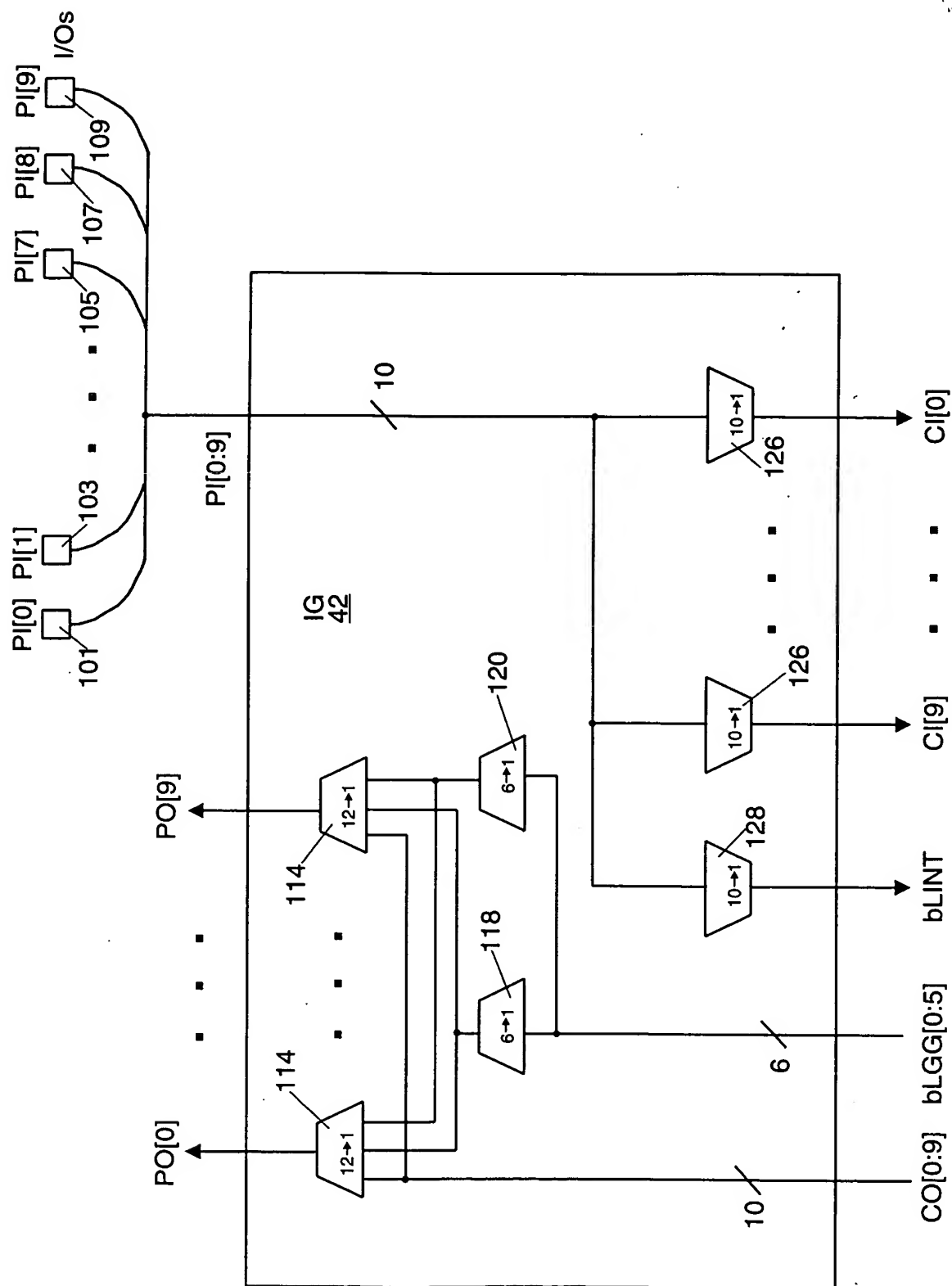


FIG. 12

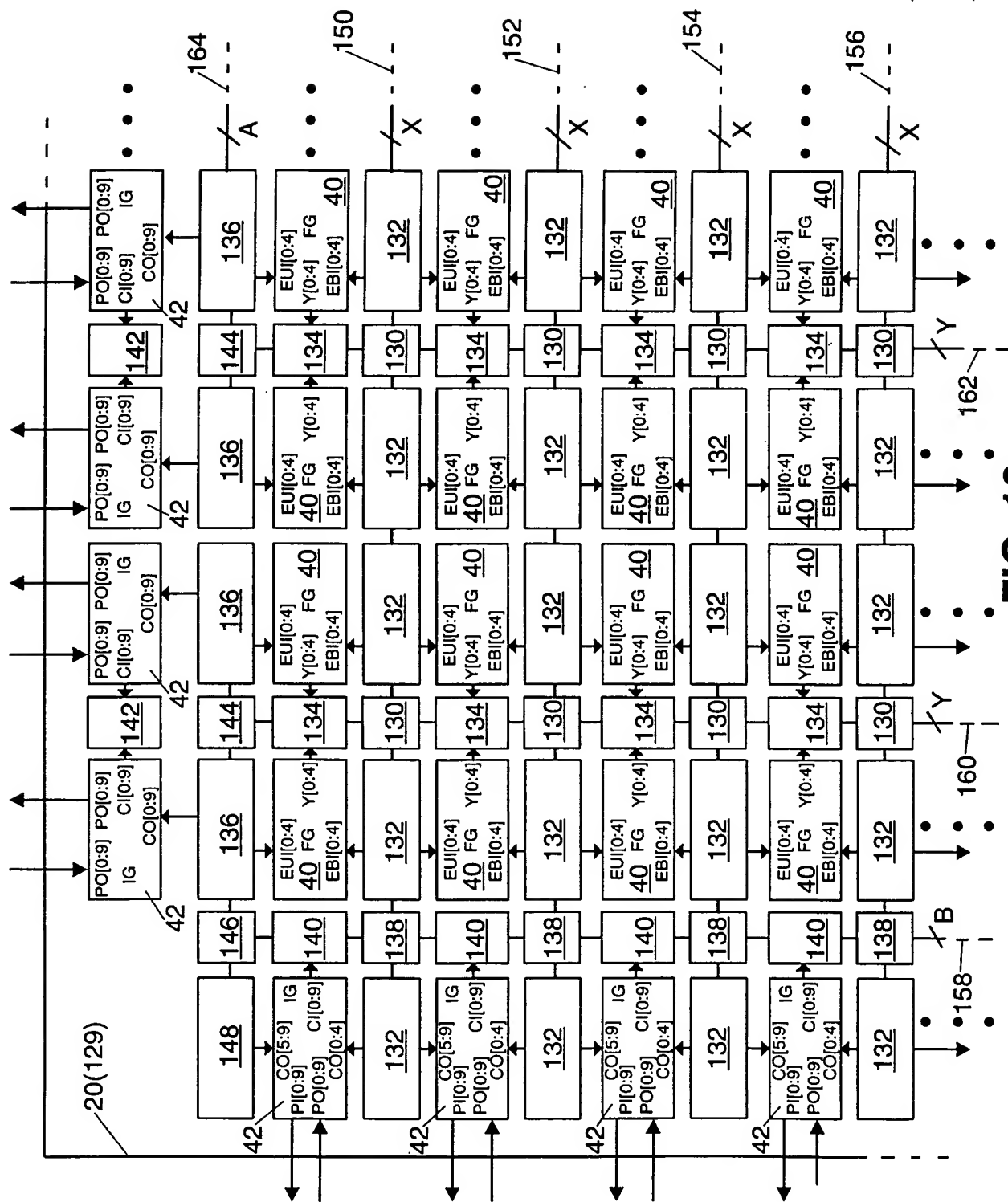


FIG. 13

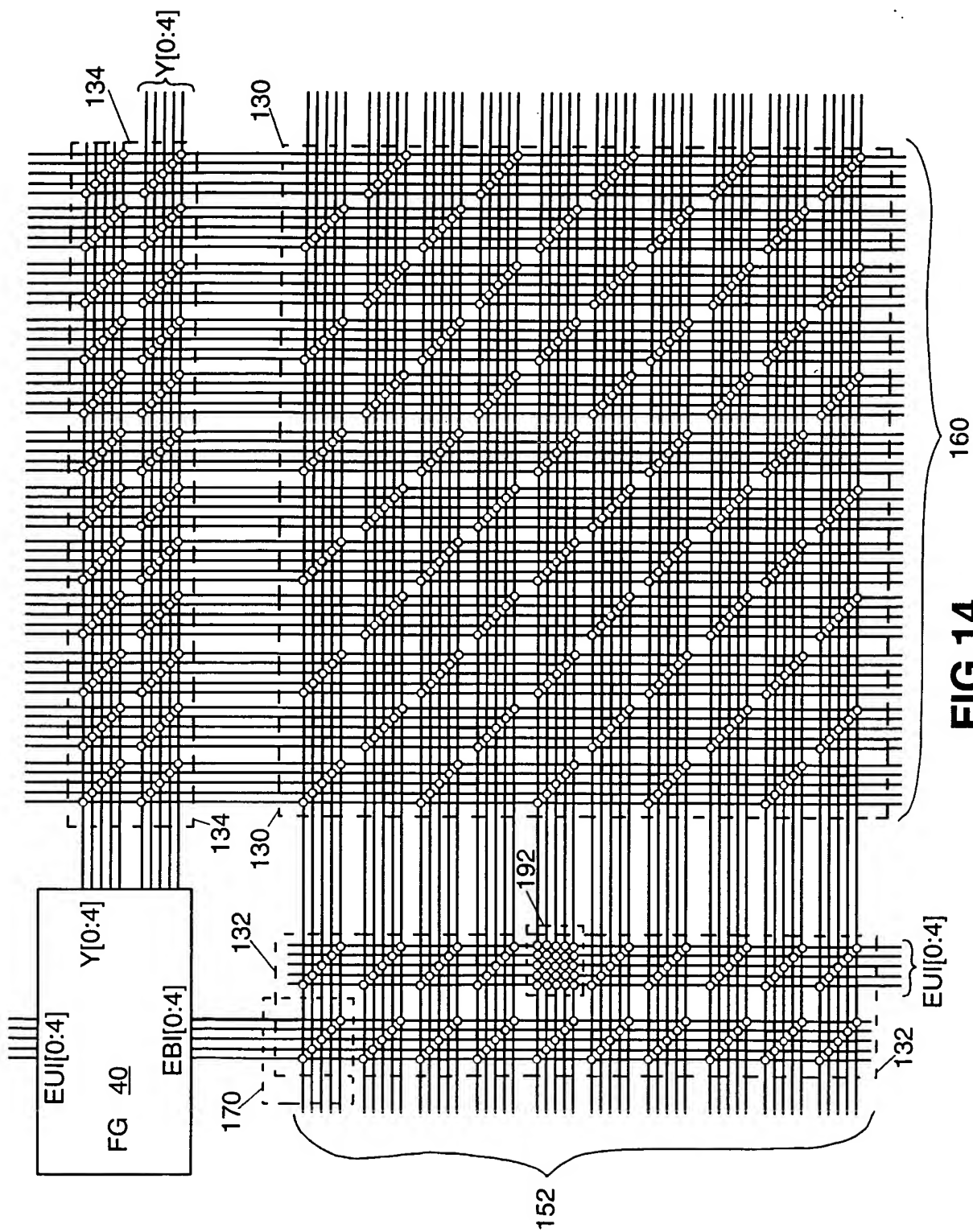


FIG.14

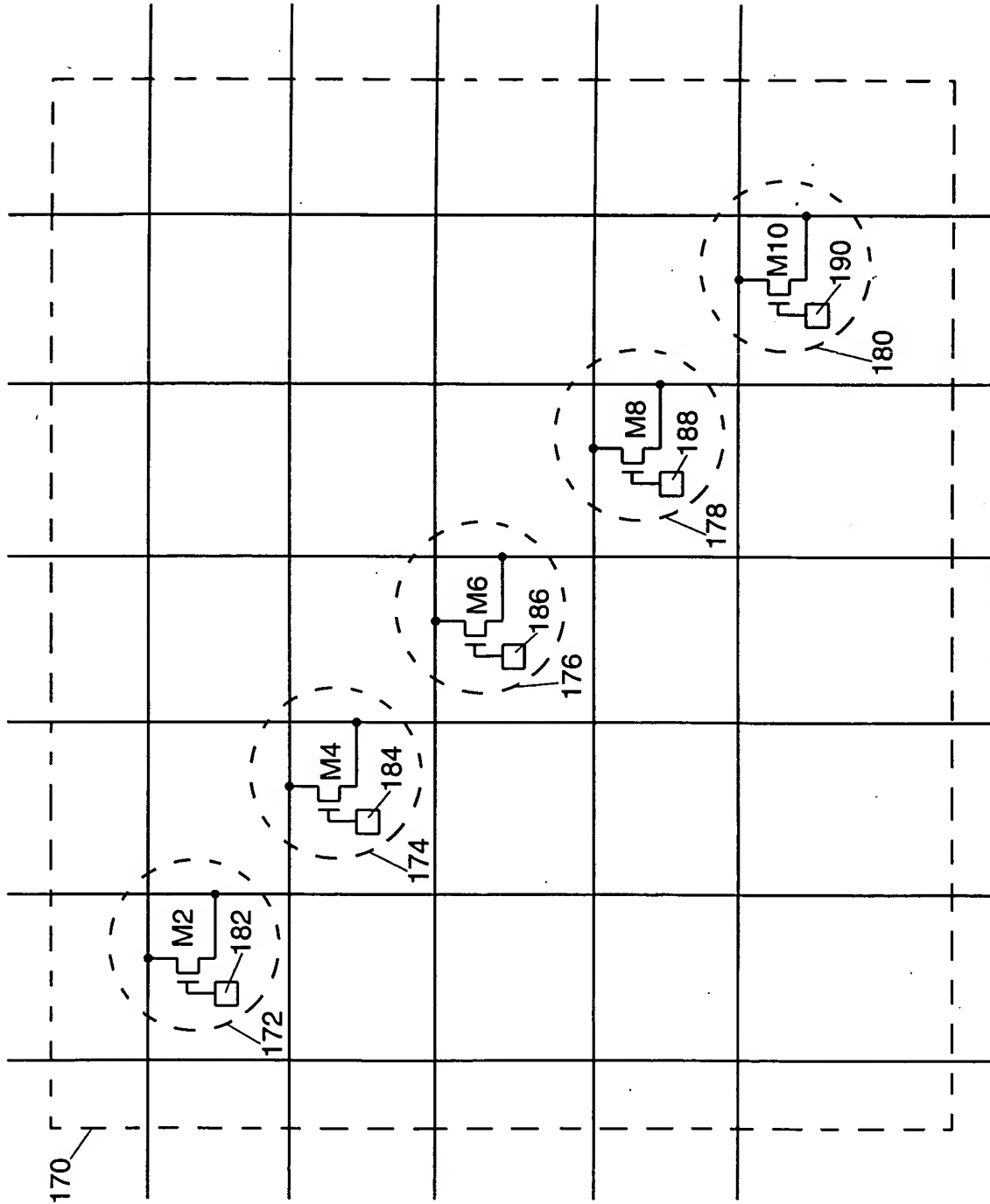


FIG. 15





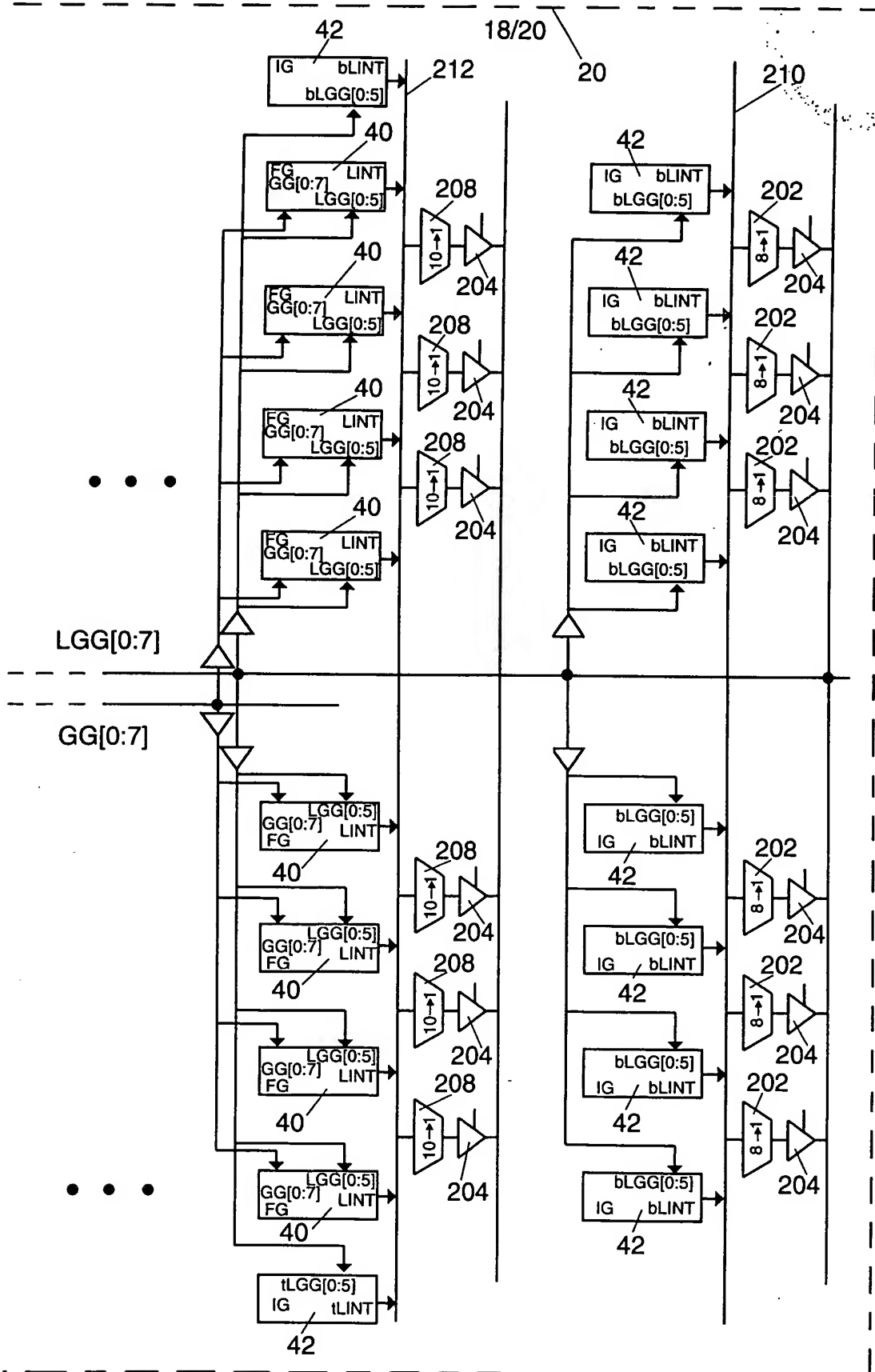
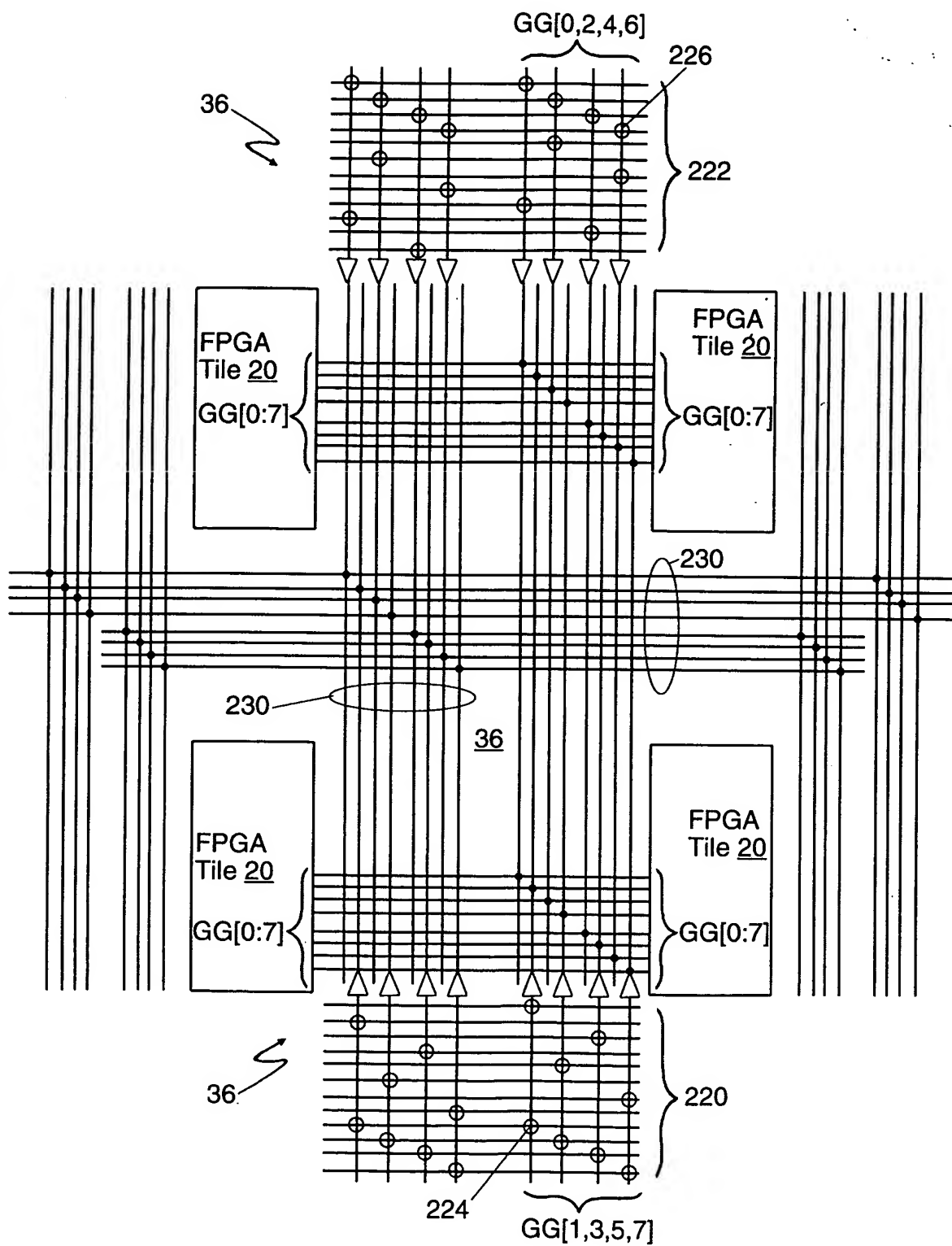


FIG. 16B

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**FIG. 17**

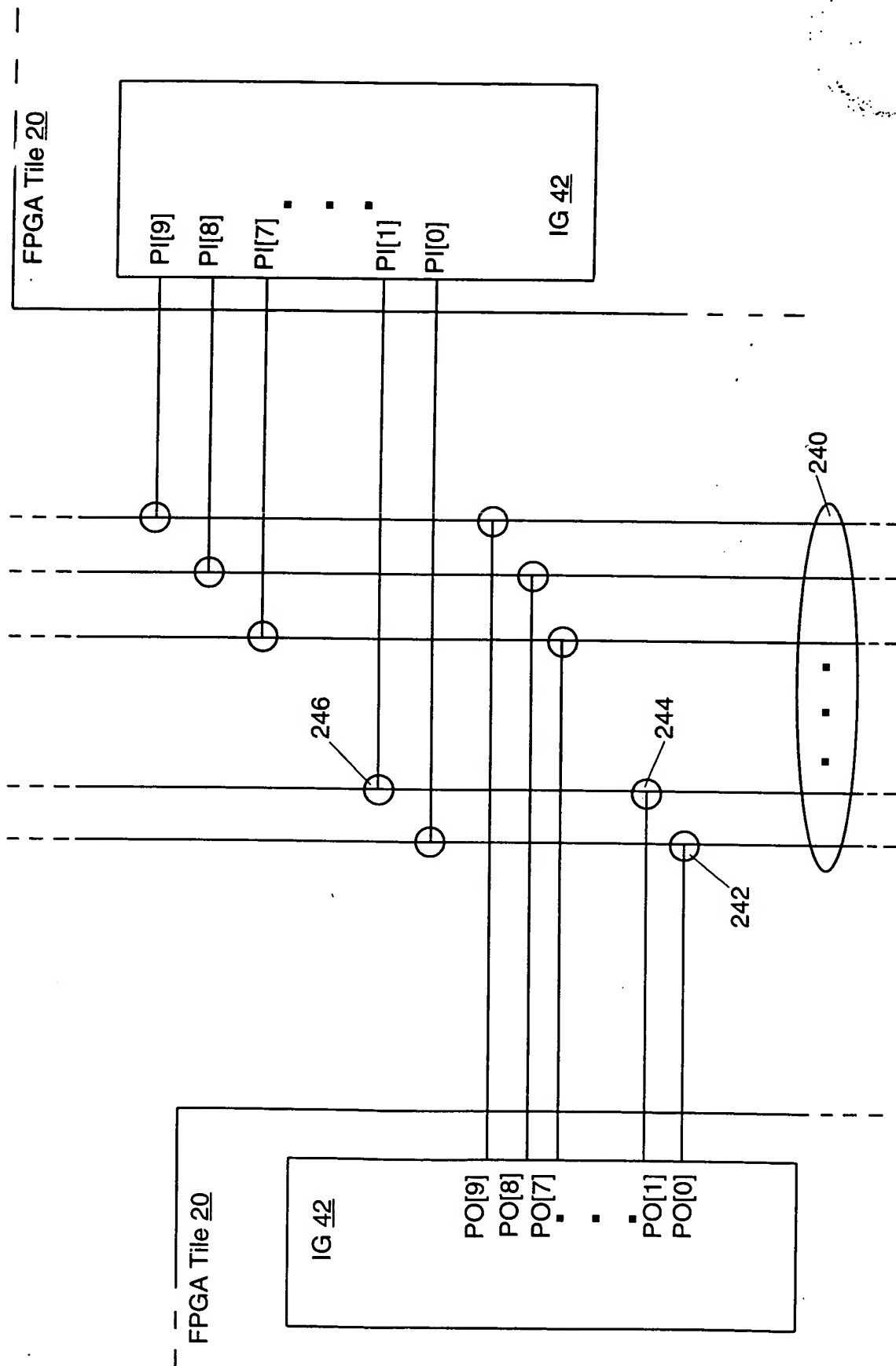


FIG. 18

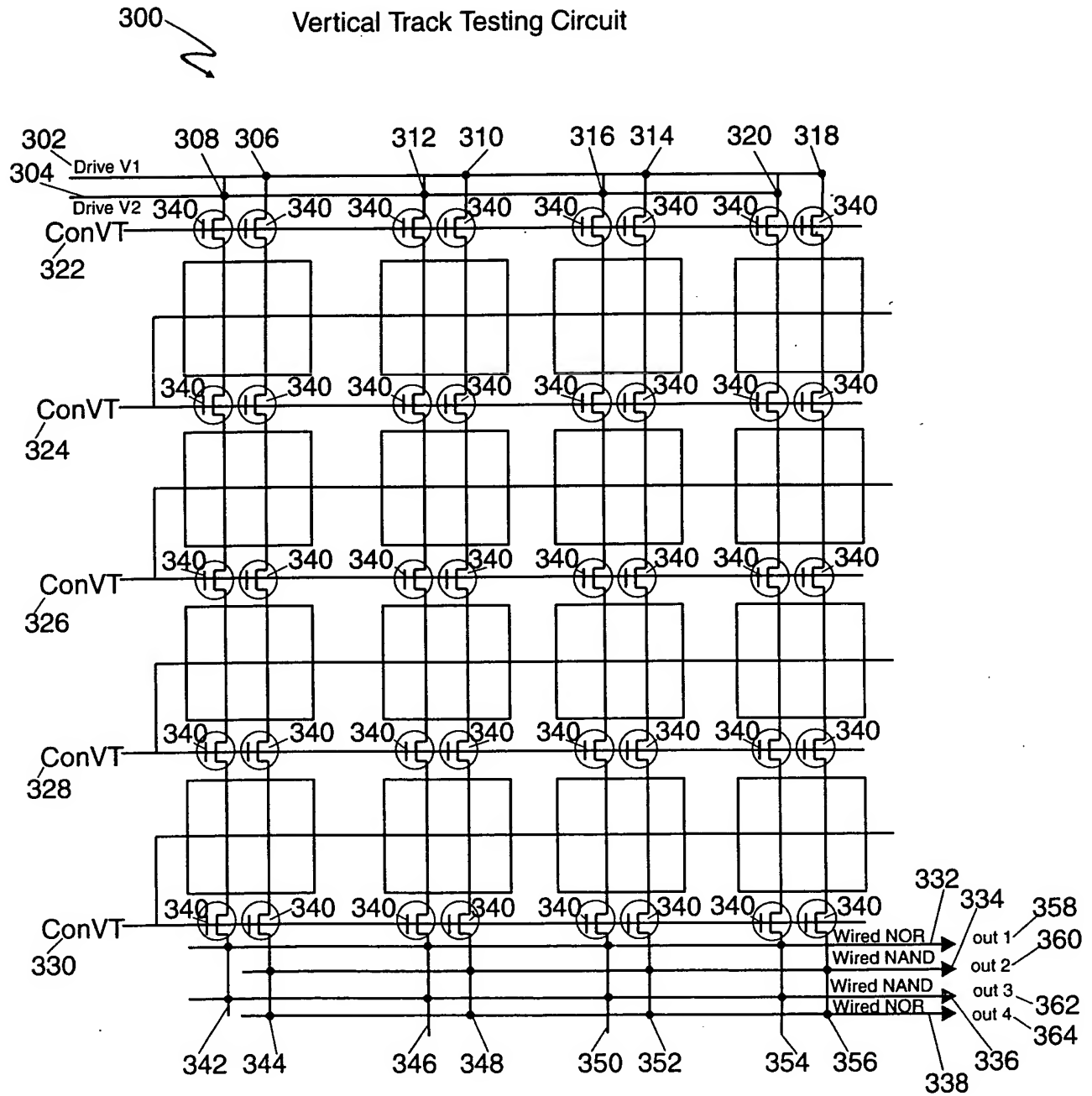


FIG. 19

Truth Table					
DriveV1	DriveV2	Out1	Out2	Out3	Out4
0	0	1	1	1	1
0	1	1	0	1	0
1	0	0	1	0	1
1	1	0	0	0	0

**FIG. 20**

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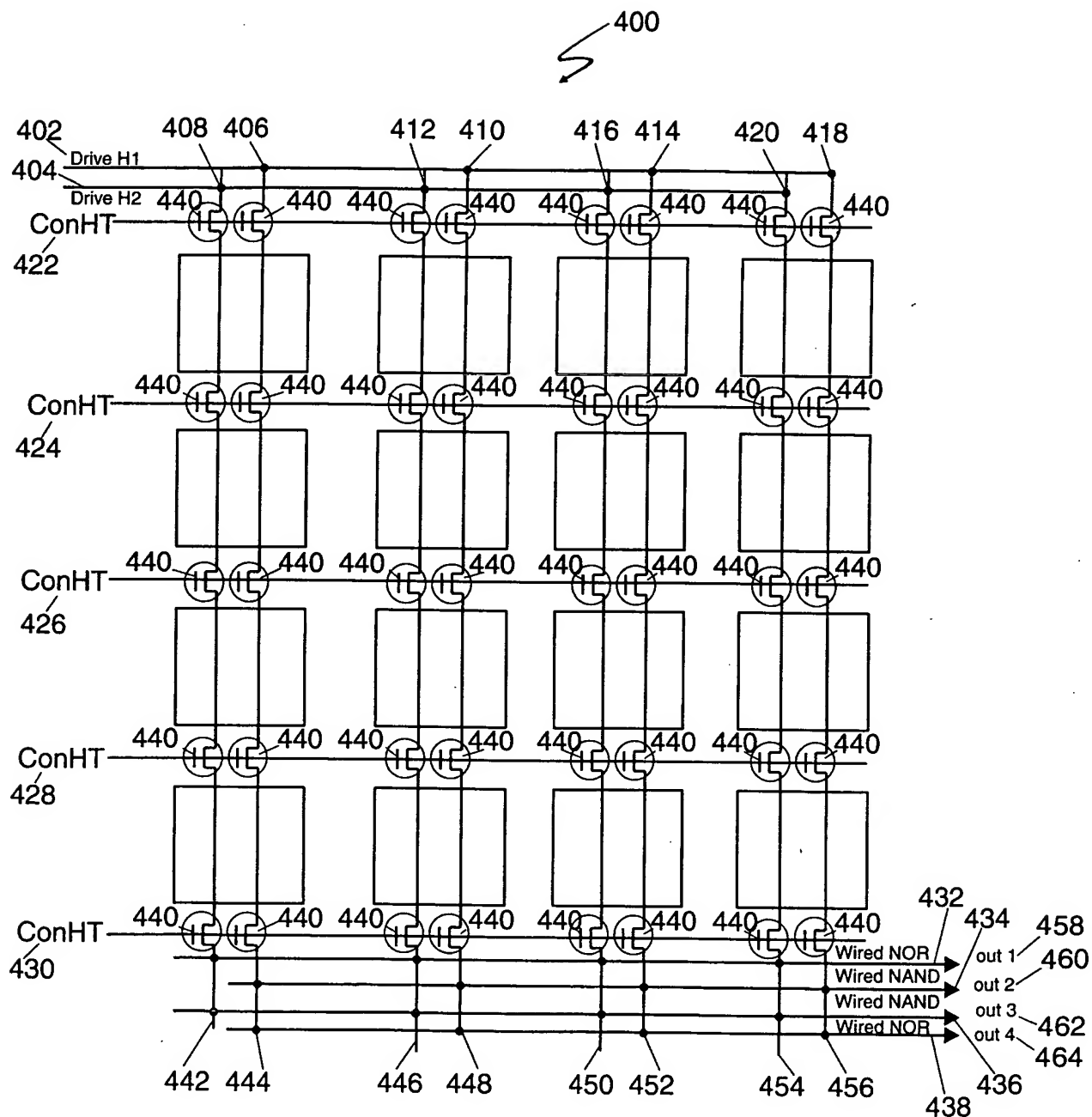


FIG. 21

Truth Table					
DriveH1	DriveH2	Out1	Out2	Out3	Out4
0	0	1	1	1	1
0	1	1	0	1	0
1	0	0	1	0	1
1	1	0	0	0	0

FIG. 22